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| AIUB | | **American International University- Bangladesh (AIUB)**  **Faculty of Engineering (EEE)** | | | | | |
|  | | |  | |  |  | |
| **Course Name:** | | | Engineering Shop | | **Course Code:** | EEE 3110 | |
| **Semester:** | | | Fall 2021-22 | | **Section:** | A | |
|  | | |  | |  |  | |
| **Assignment** | | | PCB Layout Design of the project circuit | | | | |
| **Experiment Name** | | | Automobile Interior Design PCB layout | | | | |
| **POI** | | | P.i.2.A5 | | | | |
|  | | | | | | | |
| **Performance Date:** | | |  | | | | |
|  | | |  | |  |  | |
| **Sl.** | **Student ID:** | | | **Student Name:** | | | **Secured Marks**  **(10)** |
| **1.** | **17-3471-3-2** | | | **Md.Safi Rhamatullah** | | |  |
| **2.** |  | | |  | | |  |
| **3.** |  | | |  | | |  |
| **4.** |  | | |  | | |  |
| **5.** |  | | |  | | |  |
| **6.** |  | | |  | | |  |
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**Marking Rubrics (to be filled by Instructor):**

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| --- | --- | --- | --- |
| Category | Not Satisfactory  [1-2] | Good  [3-4] | Excellent  [5] |
| **Demonstrate Individual Skills** | Demonstrates very little or almost no individual skills in project implementation. | Moderately demonstrates individual skills in achieving project goal. | Effectively and actively demonstrates individual skills in achieving project goal. |
| **Demonstrate effectiveness as a leader of the team** | Works towards group goal only when prompted or with occasional prompting. | Works towards group goal without occasional prompting; accepts and fulfills individual role within group. | Consistently and actively works toward group goal; willingly accepts and fulfills individual role within group. |
| Comments: |  | | |

**Instruction:** Please insert the snapshots as per the figure caption in the below table. Schematic and PCB layout design must be done by all group members. For remaining sections, mention the student ID responsible for contributing the corresponding figure in the test. Group leader must submit the PDF of this document and all member’s proteus file from his/her teams account.

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| --- | --- |
| Everyone will do this | **Figure 1: Schematic Diagram** |
| Everyone will do this. | **Figure 2: PCB Layout** |
| ID: | **Figure 3: Top Copper Layer** |
| ID: | **Figure 4: Bottom Copper Layer** |
| ID: | **Figure 5: Top Silk Layer** |
| ID: | **Figure 6: Bottom Silk Layer** |
| ID: | **Figure 7: 3D View Top** |
| ID: | **Figure 8: 3D View Bottom** |
| ID: | **Figure 9: Top Resist Layer** |
| ID: | **Figure 10: Bottom Resist Layer** |